

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	10/634,279	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 14:39
S5	3	((clock or carrier) adj3 recovery) and (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (positive or negative) with (transistion or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 09:56
S6	9	((clock or carrier) adj3 recovery) and (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (positive or negative) with (transition or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 09:43
S7	9	((clock or carrier) adj3 recovery) and (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (positive or negative) with (transition or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 09:53
S8	116	((clock or carrier) adj3 recovery) and polyphase and (positive or negative) and (transition or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 09:43
S9	3595	polyphase and (positive or negative) and (transition or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:20
S10	13	((clock or carrier) adj3 recovery) and (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (positive or negative) same (transition or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 09:53
S11	489	((clock or carrier) adj3 recovery) or PLL or (phase adj1 lock\$3 adj1 loop)) and (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (transistion or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 09:59
S12	99	((clock or carrier) adj3 recovery) or PLL or (phase adj1 lock\$3 adj1 loop)) same (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (transistion or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 11:36
S13	110	((data or clock or carrier) adj3 recover\$3) or PLL or (phase adj1 lock\$3 adj1 loop)) same (poly\$1phase or multi\$1phase or (multiple adj1 phase)) same (transistion or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:08

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S14	11	S13 not S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 11:40
S15	777	(((signal or data or clock or carrier) adj3 recover\$3) or PLL or (phase adj1 lock\$3 adj1 loop)) same (poly\$1phase or multi\$1phase or (multiple adj1 phase)) and (transistion or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:11
S16	3429	375/354.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:16
S17	6	S9 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:17
S18	198	(((signal or data or clock or carrier) adj3 recover\$3)) and (PLL or (phase adj1 lock\$3 adj1 loop)) same (poly\$1phase or multi\$1phase or (multiple adj1 phase)) and (transistion or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:15
S19	157	S18 not S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:15
S20	997	375/355.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:18
S21	8	S9 and S20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:18
S22	2650	375/376.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:18
S23	9	S9 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:19

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S24	689	375/326.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:18
S25	3	S9 and S24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:19
S26	140344	((polyphase with clock) or oscillator) and (transition or state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:21
S27	283	S24 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:22
S28	1419	S22 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:22
S29	314	S20 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:21
S30	408	S16 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:21
S31	4294	((polyphase with clock) or oscillator) and (transition or state) and data adj3 recover\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:31
S32	69	S24 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:25
S33	355	S22 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:22

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S34	98	S20 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 13:12
S36	396	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) with (clock or oscillator) and (transition or state) and data adj3 recover\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 07:51
S37	9	S24 and S36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 13:05
S38	4	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) with (clock or oscillator) and (transition or state) and ((data or signal or clock) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with operat\$3 or logic or gate) same switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 07:57
S39	1	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) with (clock or oscillator) and (transition or state) and ((data or signal or clock) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with (operat\$3 or logic or gate)) same switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 07:57
S40	1	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) with (clock or oscillator) and ((data or signal or clock) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with (operat\$3 or logic or gate)) same switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 07:58
S41	1	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) with (clock or oscillator) and ((data or signal or clock or carrier or frequency) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with (operat\$3 or logic or gate)) same switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 07:58
S42	1	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) with (clock or oscillator) and ((data or signal or clock or carrier or frequency) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with (operat\$3 or logic or gate)) same (switch\$3 or multiplex)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 08:01
S43	1	(poly\$1phase or multi\$1phase or (multiple adj1 phase)) and ((data or signal or clock or carrier or frequency) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with (operat\$3 or logic or gate)) same (switch\$3 or multiplex)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 08:01
S44	24	((data or signal or clock or carrier or frequency) adj3 recover\$3) same (FF or flip adj1 flop) same ("AND" with (operat\$3 or logic or gate)) same (switch\$3 or multiplex)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 08:01